



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Naoshi ADACHI et al.

Serial No.: 10/750,883

Filed: January 5, 2004

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Art Unit: 2826

Examiner: Williams, Alexander

For: HEAT TREATMENT JIG FOR SEMICONDUCTOR SUBSTRATE AND METHOD OF  
HEAT TREATING SEMICONDUCTOR SUBSTRATE

**ELECTION**

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated March 28, 2006, Applicants hereby elect the Group I invention, claims 1-5 and 8-10.

It is noted that the restriction requirement grouped claims 8-10 with the method group of claims 6 and 7. However, these claims are apparatus claims and are properly grouped with the apparatus and should be examined with claims 1-5.

An early examination on the merits is respectfully requested.

Respectfully submitted,

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By

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Docket No.: 12054-0023  
Date: April 28, 2006